

IN THE CLAIMS:

Please amend the claims as follows:

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1 --1. (Amended) A method in a data processing system for simulating
2 a hardware fault occurring on an expansion card, said expansion
3 card coupled to a processing unit in said system utilizing a bus;
4 said method comprising the steps of:

5 specifying said hardware fault to simulate;

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6 determining a signal to output utilizing said bus to simulate
7 said hardware fault occurring on said expansion card; [and]

8 creating an analog voltage signal representative of said
9 specified hardware fault; and

10 outputting said analog voltage signal during operation of said
11 expansion card, wherein said hardware fault occurring on said
12 expansion card is simulated.--

sub B1
1 2. (Unchanged) The method according to claim 1, wherein said step
2 of determining a signal to output utilizing said bus to simulate
3 said hardware fault occurring on said expansion card further
4 comprises the step of determining a signal to output utilizing a
5 PCI bus to simulate said hardware fault occurring on said expansion
6 card.

1 *2* *3* *4* --3. (Amended) The method according to claim 1, further comprising
the step of prior to outputting said analog voltage signal,
determining a proper response of said system to said hardware
fault.--

1 *2* *3* *4* --4. (Amended) The method according to claim 3, further comprising
the step of in response to outputting said analog voltage signal,
determining if said system responded properly to said hardware
fault.--

1 *2* *3* *4* 5. (Unchanged) The method according to claim 4, further comprising
the step of determining a line of said bus which is associated with
said hardware fault.

1 *2* *3* *4* --6. (Amended) The method according to claim 5, further comprising
the step of outputting said analog voltage signal during operation
of said expansion card utilizing said line of said bus.--

1 *2* *3* *4* --7. (Amended) The method according to claim 6, further comprising
the step of determining a test voltage level for said analog
voltage signal, wherein said test voltage level is a voltage level
required to simulate said hardware fault.--

1 *2* *3* *4* --8. (Amended) The method according to claim 7, further comprising
the step of outputting said analog voltage signal having said test
voltage level during operation of said expansion card utilizing
said line of said bus.--

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9. (Unchanged) The method according to claim 8, wherein said step
of determining a signal to output utilizing said bus to simulate
said hardware fault occurring on said expansion card further
comprises the step of determining a signal to output utilizing a
PCI bus to simulate said hardware fault occurring on said expansion
card.

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--10. (Amended) A data processing system for simulating a hardware
fault occurring on an expansion card, said expansion card coupled
to a processing unit in said system utilizing a bus, comprising:

means for specifying said hardware fault to simulate;

means for determining a signal to output utilizing said bus to
simulate said hardware fault occurring on said expansion card;
[and]

means for creating an analog voltage signal representative of
said specified hardware fault; and

means for outputting said analog voltage signal during
operation of said expansion card, wherein said hardware fault
occurring on said expansion card is simulated.--

sub B1

11. (Unchanged) The method according to claim 10, wherein said means
for determining a signal to output utilizing said bus to simulate
said hardware fault occurring on said expansion card further
comprises means for determining a signal to output utilizing a PCI
bus to simulate said hardware fault occurring on said expansion
card.

sub B1

--12. (Amended) The system according to claim 10, further comprising means prior to outputting said analog voltage signal, for determining a proper response of said system to said hardware fault.--

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--13. (Amended) The system according to claim 12, further comprising means responsive to outputting said analog voltage signal, for determining if said system responded properly to said hardware fault.--

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--14. (Unchanged) The system according to claim 13, further comprising means for determining a line of said bus which is associated with said hardware fault.

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--15. (Amended) The system according to claim 14, further comprising means for outputting said analog voltage signal during operation of said expansion card utilizing said line of said bus.--

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--16. (Amended) The system according to claim 15, further comprising means for determining a test voltage level for said analog voltage signal, wherein said test voltage level is a voltage level required to simulate said hardware fault.--

--17. (Amended) The system according to claim 16, further comprising means for outputting said analog voltage signal having said test voltage level during operation of said expansion card utilizing said line of said bus.--

13B

18. (Unchanged) The system according to claim 17, wherein said means
2 for determining a signal to output utilizing said bus to simulate
3 said hardware fault occurring on said expansion card further
4 comprises means for determining a signal to output utilizing a PCI
5 bus to simulate said hardware fault occurring on said expansion
6 card.